Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification

By Zainalabedin Navabi

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi

This rigorous text shows electronics designers and students how to deploy Verilog in sophisticated digital systems design.The Second Edition is completely updated -- along with the many worked examples -- for Verilog 2001, new synthesis standards and coverage of the new OVI verification library.

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification

By Zainalabedin Navabi

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi

This rigorous text shows electronics designers and students how to deploy Verilog in sophisticated digital systems design.The Second Edition is completely updated -- along with the many worked examples -- for Verilog 2001, new synthesis standards and coverage of the new OVI verification library.

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi Bibliography

- Sales Rank: #741761 in Books
- Published on: 2005-10-03
- Original language: English
- Number of items: 1
- Dimensions: 9.30" h x 1.30" w x 6.10" l, 1.10 pounds
- Binding: Hardcover
- 384 pages

⬇ **Download** Verilog Digital System Design: Register Transfer L ...pdf

▤ **Read Online** Verilog Digital System Design: Register Transfer ...pdf

Download and Read Free Online Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi

Editorial Review

From the Author
The book comes with a CD that contains Verilog and VHDL simulators, lecture material, and a web based electronic indexing tool that we call Verilog Circuit Navigator. To see how the navigator works, please send email to navabi@ece.neu.edu for a demo version.

From the Back Cover
LEARN VERILOG DESIGN WITH A MASTER

This rigorous tutorial shows electronics designers and students how to apply Verilog in sophisticated digital systems design, using over a hundred skill-building, fully worked-out, and simulated examples. Completely updated, the second edition covers Verilog 2001, new synthesis standards, testing and testbench developments, and the new OVL verification library. You'll find out just what's involved in using Verilog hardware description language (HDL) in digital system design. HDL expert Zain Navabi explains the design process in logical sequence -- the way it's done in the real world.

Moving from simple concepts to the more complex, Navabi interprets Verilog constructs related to design stages and design abstractions, including behavioral description, dataflow description, and structure description. With emphasis on the concepts of concurrency and delay in hardware, the text helps you grasp the essence of HDLs. Clear specification of learning objectives at the beginning of each chapter and end-of-chapter problems focus attention on key concepts.

If mastery of design with Verilog is the goal, Zain Navabi's *Verilog Digital System Design* is the tool.

MUST-HAVE CD INCLUDED:

- Verilog and VHDL simulators
- Synthesis tools
- Mixed-level logic and Verilog design environment
- FPGA design tools and environments from Altera
- Related tutorials and standards
- All worked examples from the book, including testbench and simulation-run reports for every example
- Complete CPU examples with Verilog code and software tools
- OVL verification libraries and tutorials

THE BEST VERILOG TUTORIAL -- UPDATED:

- Verilog 2001, step by step in examples and text
- OVL verification library
- New synthesis standards
- New chapter on testbench development and verification
- Problem set in each chapter encourages review and test of key concepts
- Instructor's manual and lecture slides available for class use

About the Author

Zainalabedin Navabi (Boston, MA) is Professor of Electrical and Computer Engineering at Northeastern University. He is the author of *Verilog Digital System Design.*

Users Review

From reader reviews:

Stephen Hancock:

Reading a e-book tends to be new life style in this particular era globalization. With reading you can get a lot of information that could give you benefit in your life. Along with book everyone in this world may share their idea. Textbooks can also inspire a lot of people. A lot of author can inspire their very own reader with their story or even their experience. Not only the storyplot that share in the books. But also they write about the ability about something that you need example. How to get the good score toefl, or how to teach your young ones, there are many kinds of book that you can get now. The authors in this world always try to improve their skill in writing, they also doing some study before they write to their book. One of them is this Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification.

James Bassler:

The actual book Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification has a lot details on it. So when you make sure to read this book you can get a lot of profit. The book was published by the very famous author. Tom makes some research just before write this book. This book very easy to read you can get the point easily after looking over this book.

Frank Arnett:

Within this era which is the greater individual or who has ability in doing something more are more important than other. Do you want to become certainly one of it? It is just simple way to have that. What you need to do is just spending your time not very much but quite enough to get a look at some books. Among the books in the top list in your reading list is definitely Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification. This book and that is qualified as The Hungry Hills can get you closer in growing to be precious person. By looking upward and review this book you can get many advantages.

Gloria Quinones:

Some individuals said that they feel bored stiff when they reading a e-book. They are directly felt the item when they get a half parts of the book. You can choose the actual book Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification to make your current reading is interesting. Your own personal skill of reading ability is developing when you including reading. Try to choose simple book to make you enjoy to see it and mingle the sensation about book and examining especially. It is to be very first opinion for you to like to available a book and examine it. Beside that the publication Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification can to be a newly purchased friend when you're experience alone and confuse with the information must you're doing of the

time.

Download and Read Online Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi #MT32K1Z46OW

Read Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi for online ebook

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi Free PDF d0wnl0ad, audio books, books to read, good books to read, cheap books, good books, online books, books online, book reviews epub, read books online, books to read online, online library, greatbooks to read, PDF best books to read, top books to read Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi books to read online.

Online Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi ebook PDF download

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi Doc

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi Mobipocket

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification By Zainalabedin Navabi EPub