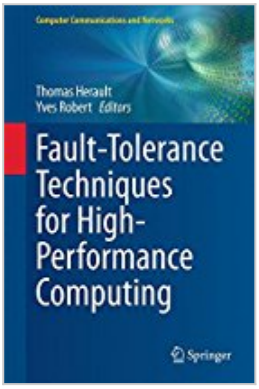

Fault-Tolerance Techniques for High-Performance Computing (Computer Communications and Networks)

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This timely text presents a comprehensive overview of fault tolerance techniques for high-performance computing (HPC). The text opens with a detailed introduction to the concepts of checkpoint protocols and scheduling algorithms, prediction, replication, silent error detection and correction, together with some application-specific techniques such as ABFT. Emphasis is placed on analytical performance models. This is then followed by a review of general-purpose techniques, including several checkpoint and rollback recovery protocols. Relevant execution scenarios are also evaluated and compared through quantitative models. Features: provides a survey of resilience methods and performance models; examines the various sources for errors and faults in large-scale systems; reviews the spectrum of techniques that can be applied to design a fault-tolerant MPI; investigates different approaches to replication; discusses the challenge of energy consumption of fault-tolerance methods in extreme-scale systems.

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Fault-Tolerance Techniques for High-Performance Computing (Computer Communications and Networks) From Springer Bibliography

- Sales Rank: #6749524 in Books
- Published on: 2015-07-02
- Original language: English
- Number of items: 1
- Dimensions: 9.21" h x .75" w x 6.14" l, .0 pounds
- Binding: Hardcover
- 320 pages

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